

L Number	Hits	Search Text	DB	Time stamp
-	745	LSI and 710/\$.ccls.	USPAT	2004/03/14 19:17
-	745	"(LSI)" and 710/\$.ccls.	USPAT	2004/03/14 19:17
-	2	"(LSI)" and 710/\$.ccls. and pulsed adj interrupt	USPAT	2004/03/14 19:17
-	33	"(LSI)" and 710/\$.ccls. and pulsed	USPAT	2004/03/14 19:21
-	0	level adj sensitive adj interrupt and pulsed adj interrupt	USPAT	2004/03/14 19:22
-	58	level adj sensitive adj interrupt	USPAT	2004/03/14 19:22
-	18	pulsed adj interrupt	USPAT	2004/03/14 19:24
-	1	("5754884").PN.	USPAT	2004/03/14 19:23
-	1	("6163829").PN.	USPAT	2004/03/14 20:35
-	1	("6185652").PN.	USPAT	2004/03/14 19:23
-	1	("5983275").PN.	USPAT	2004/03/14 19:24
-	1	pulsed adj interrupt with DMA	USPAT	2004/03/14 19:24
-	5	pulsed adj interrupt and DMA	USPAT	2004/03/14 19:31
-	5	(pulsed adj interrupt) and DMA	USPAT	2004/03/14 19:32
-	0	level adj sensitive adj interrupt with DMA	USPAT	2004/03/14 19:32
-	23	level adj sensitive adj interrupt and DMA	USPAT	2004/03/14 19:32
-	787	(CPU or processor) with interrupt adj handler	USPAT	2004/03/14 20:35
-	286	(CPU or processor) with interrupt adj handler and 710/\$.ccls.	USPAT	2004/03/14 20:35
-	107	(CPU or processor) with interrupt adj handler and 710/\$.ccls. and DMA	USPAT	2004/03/14 20:36
-	30	(CPU or processor) with interrupt adj handler and 710/\$.ccls. and DMA and edge\$	USPAT	2004/03/14 20:40
-	0	(CPU or processor) with internal adj3 interrupt adj handler and 710/\$.ccls.	USPAT	2004/03/14 20:40
-	9	(CPU or processor) with internal with interrupt adj handler and 710/\$.ccls.	USPAT	2004/03/14 20:40